PULSE GENERATORS & DATA GENERATORS

Data Generator System HP 80000

- · Clock or data rate up to 1 GHz
- 16 or 128 Kb memory per channel
- Delay range ±2 ns, 2 ps resolution.

- Amplitude up to 2.5 V or 3.0 V in ~2 V to 3 V level window
- Color touchscreen, mouse, keyboard, and knob
- 4 to 80 channels



HP 80000 Data Generator System with 2 strobe and 16 data channels. With the new expansion frames, up to 80 channels can be configured



Expansion frame 128 kbit/channel



HP 80000 Data Generator System

When you need multiple lines of real data to characterize your device, the HP 80000 system delivers everything needed to make the test complete, accurate, and reliable because it offers the kind of edge-placement precision that is usually only found in high-performance pulse generators—but at up to 1 GHz and over 80 channels!

These features—plus affordability, prbs and conformance to national and international electromagnetic regulations - have made the HP 80000 system a preferred choice when characterizing fast ICs, modules, or components such as:
 MUX, DAC, memories
 ATM, SONET/SDH, switches

as well as passive devices like HF connectors and computer back-

Rapid performance verification can be carried out with the HP 80000's prbs and the help of the HP 54120 Series oscilloscopeusing eye-pattern technique-or the HP 71600 Series BERT. The memory is segmentable so that preamble/data or initialize/data sequences can be set up.

Currently, the HP 80000 system consists of a mainframe, an expansion frame, and a choice of modules so that systems with up to 80 channels can be factory-configured or retrofitted. The mainframe includes an internal clock plus synchronous start/stop logic. It has friendly HP 16500-type user interface and supports a keyboard, mouse, two internal disk drives and HP-IB. The mainframe, like the extender, has room for five modules.

The four-channel, 1 GHz data modules provide RZ (50% duty cycle) and NRZ formats. Edges can be positioned with 2 ps resolution in a ±2 ns window; independent of clock rate. There is a choice of 16 or 128 Kb per channel.

The clock/strobe modules process the mainframe clock to provide normal and complement clock outputs. They also have two channels which can be used as strobes, as clock dividers for multi-phase signals, or for data patterns. 16/128 Kb modules are available.

Specifications

For more information, please consult the HP 80000 Data Generator System brochure p/n 5091-2917 and the HP 80000 Data Generator System technical data sheet p/n 5091-2916.

HP E2900A Mainframe

Internal Clock: 7.8 MHz to 1 GHz, 1 ns to 128 ns

External Clock Input

Range: 10 MHz to 3 GHz

External input: Start, start-stop, gate External reference input: Rear panel

Auxiliary output: Rear panel

General Characteristics

Peripheral Input Devices: Connected via the HP-HIL connector

Built-in Disk Drives: Format 3½-in 1.44 MB HD

HP-IB Capabilities: IEEE 488.1 and 488.2, 1987; SCPI 1992.0

RS-232C Capabilities: Operates as DTE

HP E2901A Expansion Frame: Controlled by HP E2900A main-

frame, all specifications apply.

HP E2902A 1 GHz Clock/Strobe Module

Clock Capabilities

Continuous clock: The clock is always active, independent of the

data sequence.

Switched clock: The clock is active during the data sequence and

stops synchronously when the sequence is completed.

Strobe Capabilities

Clock and clock divider: Each strobe channel can be used as an additional clock output to generate phase-shifted signals.

Data Patterns

Data format: DNRZ, DRZ (50% duty cycle)

Memory depth: 16 Kb

Data sequence: Preamble data, cycle data, zero run (if data chan-

nels are in mixed mode)

HP E2903A 1 GHz Data Module

Data Patterns

Data format: DNRZ, DRZ (50% duty cycle)

Memory depth: 16 Kb

Data sequence: Preamble data, cycle data, PRWS23 (random data

based on PRBS sequence 223-1)

PRBS Patterns

223-1 (CCITT O.151)

 $2^{n}-1$ where n = 7, 10, 11

Zero Substitution: Zeros can be substituted for data to extend the longest run of zeros.

Variable Mark Density: The ratio of 1s to total bits can be set to 1/8, 1/4,

External Clock Input: The external clock input on the data module is used to generate asynchronous data streams with respect to the mainframe clock.

External Input (start, gate): The external input on the data module is used to start and gate the data stream.

HP E2905A 128 kB 1 GHz Clock Strobe Module

Memory Depth: 128 k bit

Other specifications as HP E2902A

HP E2906A 128 kb 1 GHz Data Module

Memory Depth: 128 k bit

Other specifications as HP E2903A

Clock, Strobe, and Data Timing

Delay: Clock, strobe, and data delay variation per channel vs. other channel

atta, s

Range: $\pm 2.00 \text{ ns}$

Resolution: 2 ps

Accuracy (delay variation): $\pm 80 \text{ ps}$ Repeatability: 4 times better than accuracy Jitter, rms: <25 ps (<10 ps typical) Clock, Strobe, and Data Outputs

Terminations Selectable: 50 Ω to GND, 50 Ω to -2 V, into open circuit

Levels: All specifications apply for outputs terminated with 50 Ω to GND.

Resolution: 10 mV

High-Level Range: -1.5 V to 3.0 V Low-Level Range: -2.0 V to 2.5 V

Minimum amplitude 0.5 V, maximum amplitude 2.5 V, 3 V for

HP E2905A and HP E2906A Level Accuracy: $3\% \pm 50 \text{ mV}$ Overshoot, Ringing: 10% ±30 mV Impedance: 50Ω nominal

Supplementary Information: 50Ω to $\pm 2 V$:

High-level range: -1.5 V to 1.0 V Low-level range: -2.0 V to 0.5 V

Minimum amplitude 0.5 V, maximum amplitude 2.5 V, 3 V for HP

E2905A and HP E2906A

Into Open Circuit: High-level range: -2.5 V to 4.5 VLow-level range: -3.5 V to 3.5 V

Transition Times: 10/90 of amplitude: <250 ps (<150 ps typical)

20/80 of amplitude: <150 ps

Operating Environment

Ordering Information

Storage Temperature: -40° C to $+70^{\circ}$ C Operating Temperature: +15° C to +40° C

Humidity: Up to 80% relative humidity at +30° C

Power: $100-120/220-240 \text{ V rms}, \pm 10\%, 900 \text{ VA max}, 47-63 \text{ Hz}$ Weight: Net, 32 kg (71.1 lb) + $(2 \text{ kg} (4.44 \text{ lb}) \times \text{number of modules});$ shipping, 56 kg (124.5 lb) + (4 kg (8.89 lb) × number of modules)

Size: 323 mm W × 426 mm H × 602 mm D (12.7 in × 16.8 in × 23.7 in)

HP 80000 Data Generator System Components	
HP E2900A Mainframe	\$17.200
HP E2901A Expansion Frame	\$10,400
HP E2902A 1 GHz Clock/Strobe Module	\$9,900
HP E2903A 1 GHz Data Module	\$12,900
HP E2905A 128 kb 1 GHz Clock/Strobe Module	Ψ12,717
HP E2906A 128 kb 1 GHz Data Module	
Accessories	
HP 46060B HP-HIL Mouse	di s essa.
	\$179
HP 46021A HP-HIL Keyboard	\$155
HP 15432B 250 ps Transition Time Converter	\$320
HP 15433B 500 ps Transition Time Converter	\$320
HP 15434B 1 ns Transition Time Converter	\$320
HP 15438 2 ns Transition Time Converter	\$320
HP 1250-1462 Adapter SMA(m) to SMA(f)	\$25
HP 8120-4948 50 Ω Cable, SMA (m-m)	\$210
HP 8710-1582 Torque Wrench 5 in/lb	\$260
	CF sim CPC 2

\$950

HP 1182A/1181A Testmobile Carts for Instruments